

FIG. 1 is a block diagram of a system 10. The system 10 includes a central processing unit 11, a memory unit 21, a storage unit 31, and an input/output unit 41. The central processing unit 11 is connected to the memory unit 21, the storage unit 31, and the input/output unit 41. The memory unit 21 is connected to the storage unit 31. The storage unit 31 is connected to the input/output unit 41.

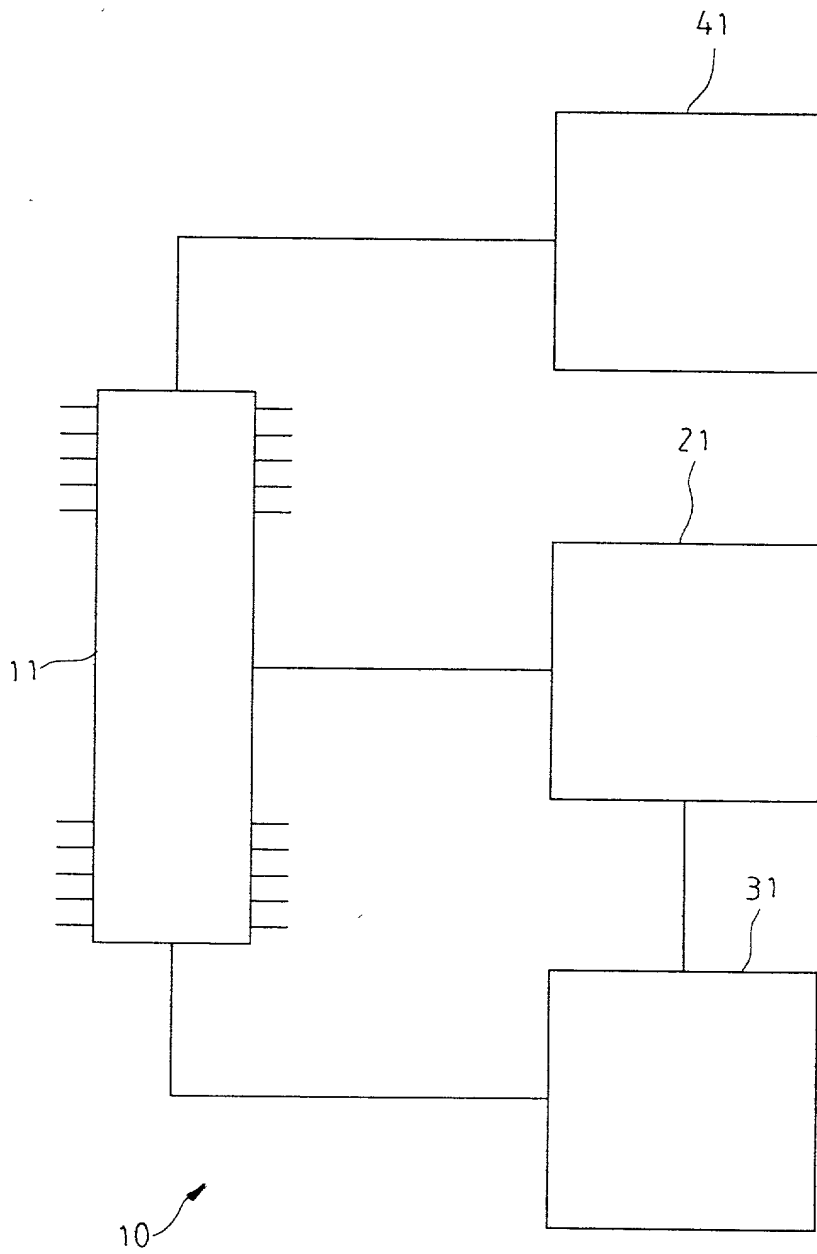
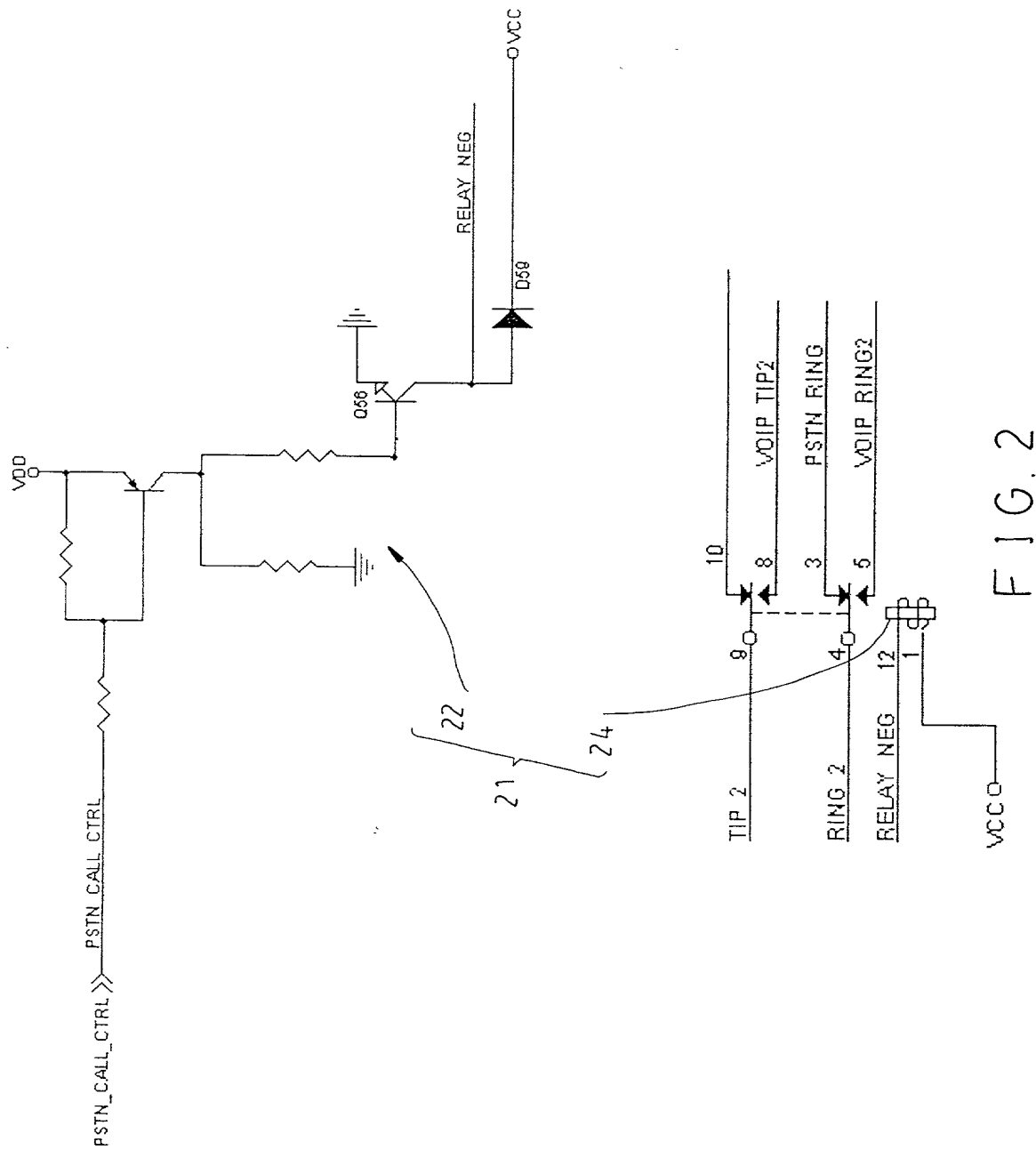


FIG. 1



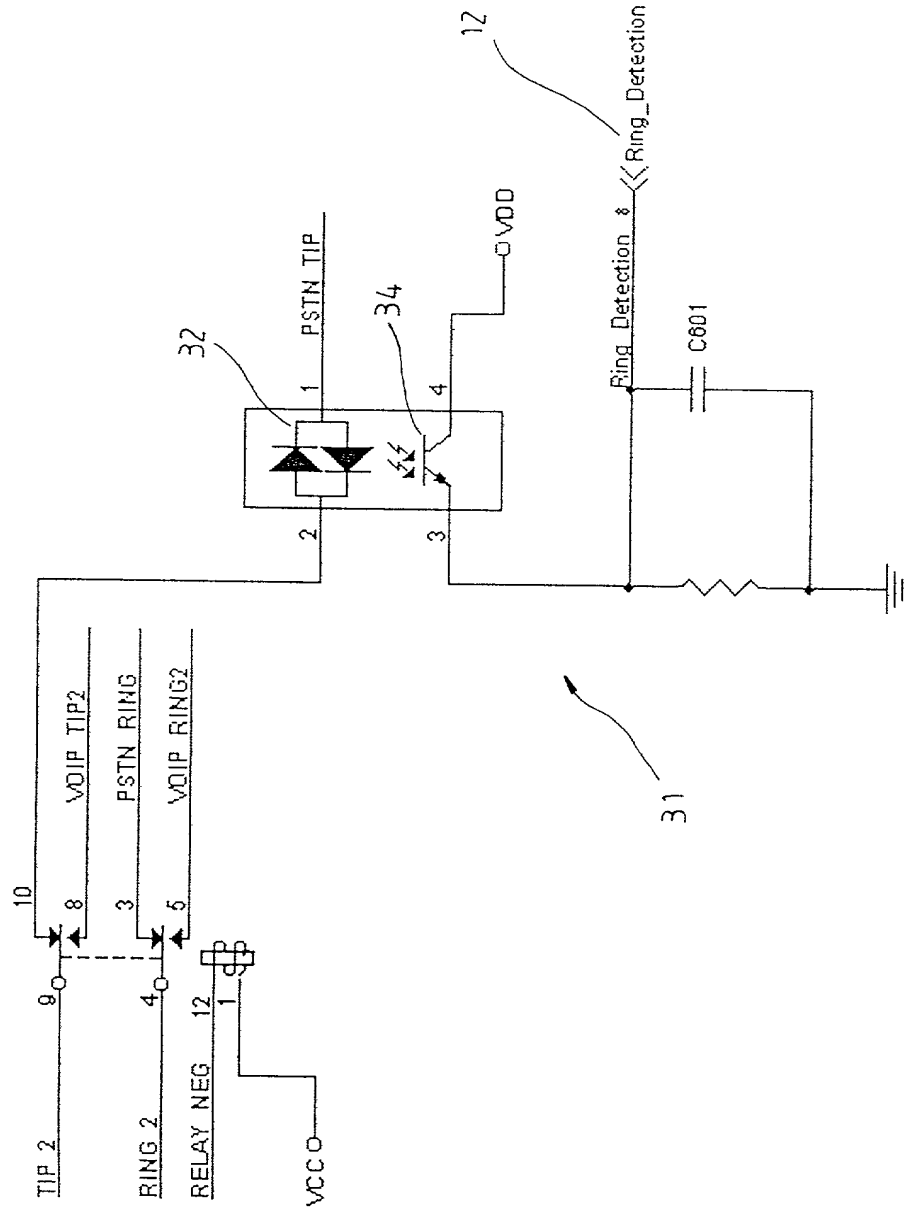


FIG. 3

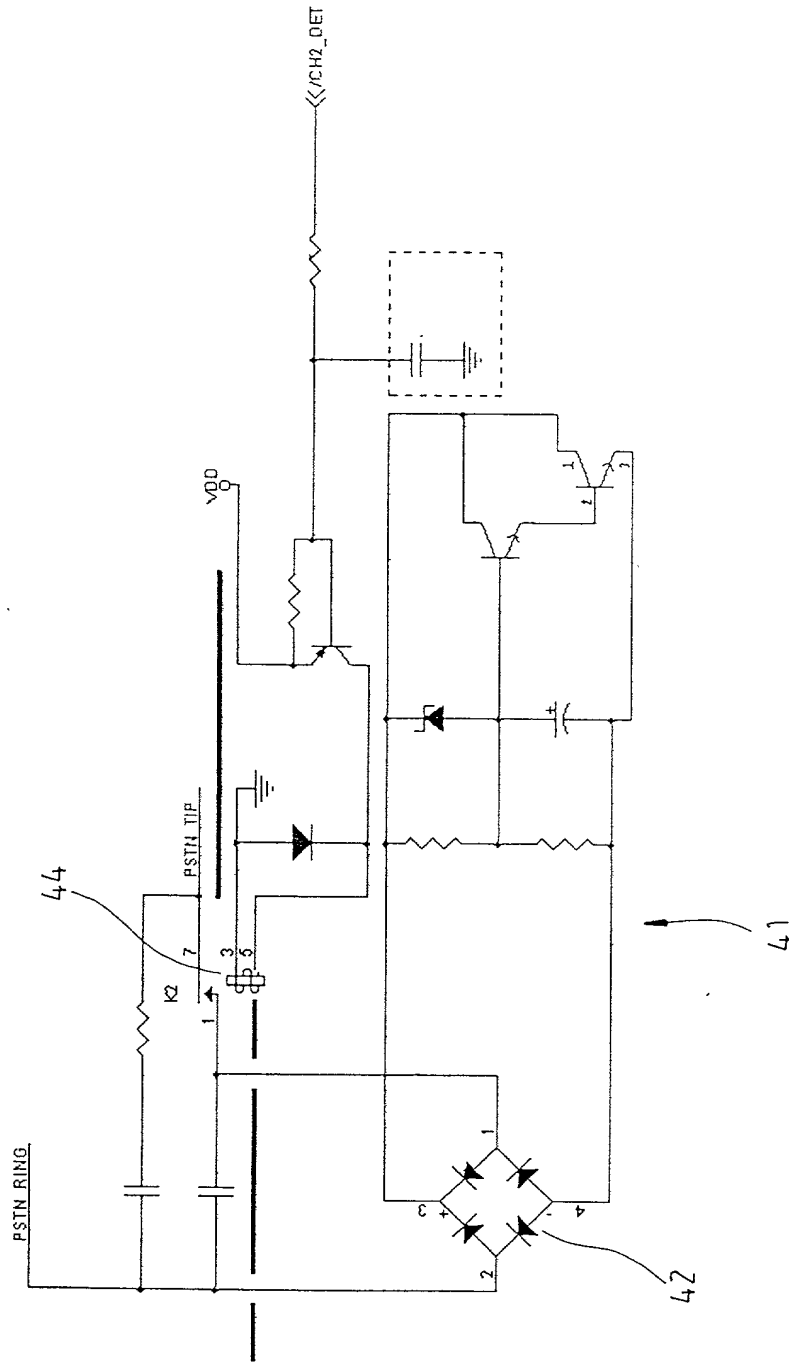


FIG. 4

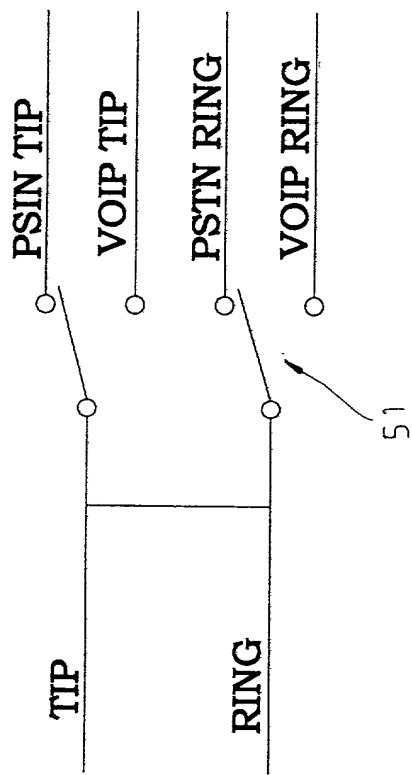


FIG. 5

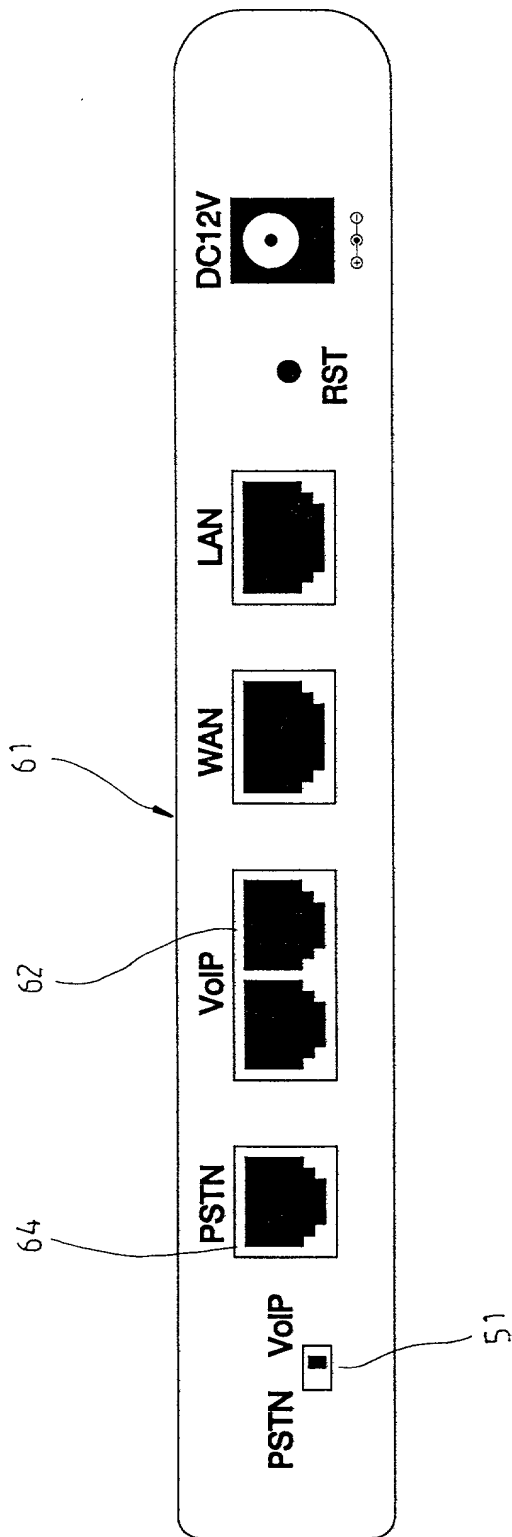


FIG. 6

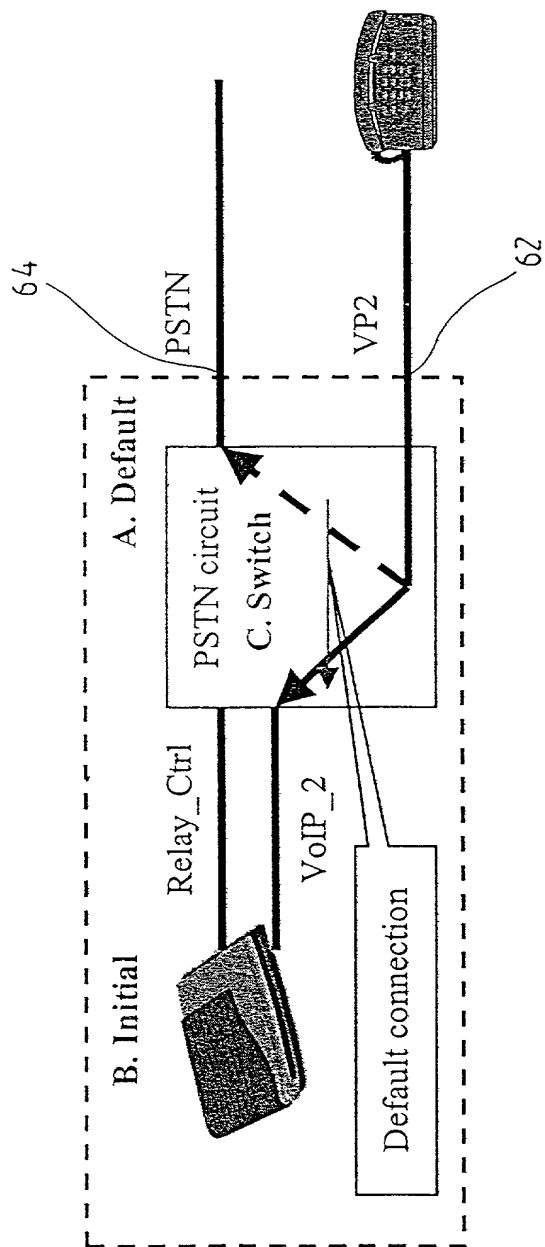


FIG. 7

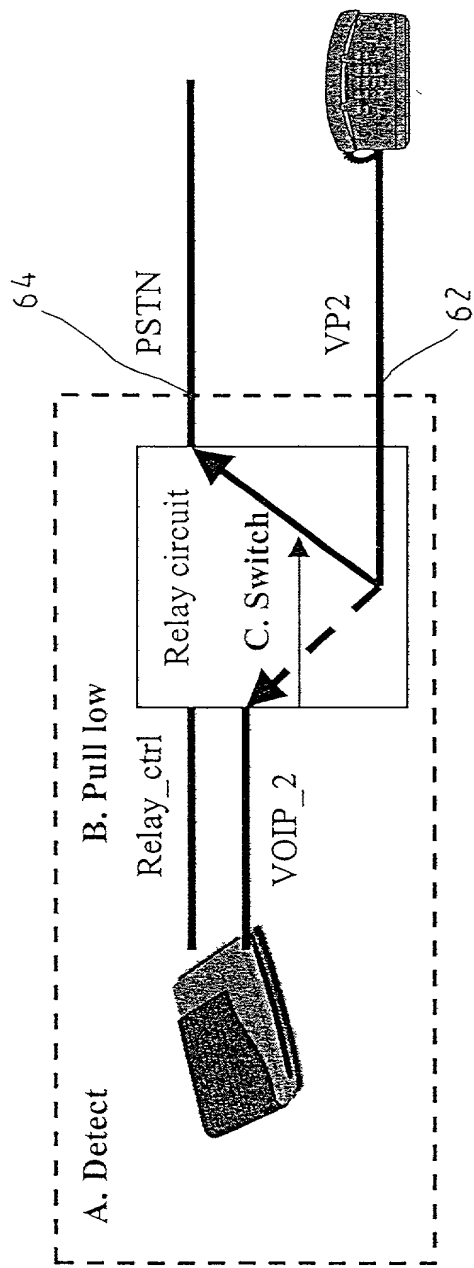


FIG. 8